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BOX PATENT APPLICATION
ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D. C. 20231

Re: NEW DESIGN PATENT APPLICATION
Attorney Docket No.: 3905
Inventor: Takao NAKAMURA et al.
Title: SEMICONDUCTOR LIGHT-EMITTING DEVICE,
METHOD OF MANUFACTURING TRANSPARENT
CONDUCTOR FILM AND METHOD OF MANUFAC-
URING COMPOUND SEMICONDUCTOR LIGHT-
EMITTING DEVICE

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Dear Sir:

I am enclosing a New Patent Application comprising:

- 1) a) 18 pages of specification; 15 claims, 3 independent;
12 Figs. (Figs. 1 to 8,9A,9B,10,11) on 8 sheets of drawings;
an abstract;
1 return receipt postcard;
Express Mail Certificate;
b) Declaration, Power of Attorney;
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d) an Assignment Recordation Form Cover Sheet, copy of Assignment
for Recordal;
e) an Information Disclosure Statement, Form PTO-1449,
1 references.

- 2) The Priorities of earlier applications:

No. 11-107586 filed on April 15, 1999 in Japan; and
No. 11-181414 filed on June 28, 1999 in Japan

are hereby expressly claimed under 35 U.S.C. 119. Please
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- 3) The Priority Documents ARE ENCLOSED. Please acknowledge receipt
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Walter F. Fasse
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE MATTER OF THE NEW APPLICATION FOR PATENT

OF: Takao NAKAMURA et al.

SERIAL NO.: TO BE ASSIGNED

FILED: March 3, 2000

FOR: SEMICONDUCTOR LIGHT-EMITTING DEVICE,
METHOD OF MANUFACTURING TRANSPARENT
CONDUCTOR FILM AND METHOD OF MANUFAC-
URING COMPOUND SEMICONDUCTOR LIGHT-
EMITTING DEVICE

ASSISTANT COMMISSIONER FOR PATENTS
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March 3, 2000

TYPE OF PAPER BEING DEPOSITED: **A NEW PATENT APPLICATION**,
comprising 18 pages of specification, 15 claims on 3 pages, an
abstract, a Declaration, 12 Figs. (Figs. 1 to 9,9A,9B,10,11) on
8 sheets of drawings, my check # 2194 in the amount of \$730.00
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Docket # 2005
JAN. Takao Nakamura
Hideki Matsubara
Filed: 03/03/00

TITLE OF THE INVENTION

Semiconductor Light-Emitting Device, Method of Manufacturing
Transparent Conductor Film and Method of Manufacturing Compound
Semiconductor Light-Emitting Device

5 BACKGROUND OF THE INVENTION

Field of the Invention

The present invention generally relates to a semiconductor light-emitting device, and more specifically, it relates to a semiconductor light-emitting device so improved that a large quantity of light is extractable.

10 The present invention also relates to a method of manufacturing a transparent conductor film, and more specifically, it relates to a method of manufacturing a transparent conductor film improved to be smooth at a lower temperature and have low resistance and high transmittance so that the cost can be reduced. The present invention further relates to a method
15 of manufacturing a compound semiconductor light-emitting device employing such a method.

Description of the Prior Art

Figs. 9A and 9B illustrate the structure of a conventional light-emitting device and the light-emitting mechanism thereof. Referring to
20 Figs. 9A and 9B, a light-emitting diode (LED) is an electrical/optical conversion type semiconductor light emitting device utilizing minority carrier injection in a p-n junction part formed by p- and n-type semiconductor crystals adjacent to each other and subsequent recombination radiation.

25 The device itself is made of a semiconductor crystal material of about 0.3 mm square, and the basic structure thereof is similar to that of an Si rectifying device, as shown in Fig. 9A.

When applying positive and negative forward voltages to the p-type crystal and the n-type crystal respectively, electrons and holes are injected
30 into a p region and an n region respectively, as shown in Fig. 9B. These minority carriers are partially recombined with majority carriers to emit light.

Such an LED has durability, long-livedness, a light weight and a

miniature size. The LED, which has been applied only to an indoor indicator lamp, is now applied to the stop lamp of a car, a road sign, a traffic light, a large-area color display or the like following improvement of efficiency and brightness and reduction of the price. Now there is a possibility of applying the LED to the headlamp of a car or a house light substituting for a fluorescent lamp. Further, development of a high-efficiency LED is expected in consideration of energy saving.

Luminous efficiency of an LED includes external quantum efficiency and internal quantum efficiency, and the efficiency of the LED is proportional to the product thereof. The internal quantum efficiency is expressed in the ratio of the number of generated photons to the number of injected electron-hole pairs. In order to improve the internal quantum efficiency, it is necessary to obtain a high-quality crystal containing a small number of defects or impurities, in order to prevent recombination of the electron-hole pairs.

The external quantum efficiency is expressed in the ratio of the number of photons radiated outward to the number of the injected electron-hole pairs. Light generated from an active layer is absorbed by the active layer itself, a substrate or an electrode, and hence can be only partially extracted into air. Further, the refractive index of the semiconductor is by far higher than that of the outside and hence most part of the light is totally reflected by the boundary between the semiconductor and the outside and returned into the semiconductor. Most of LEDs now on the market are sealed with epoxy resin having a refractive index of 1.5, in order to increase the critical angle of total reflection for extracting a larger quantity of light, in addition to the purposes of protection and anti-oxidation of the LEDs.

Fig. 10 is a conceptual diagram showing the structure of a conventional LED. An active layer 23 is formed on an n-type semiconductor layer 22 having an n electrode 21 on the back surface. A p-type semiconductor layer 24 is formed on the active layer 23. A p electrode 25 is formed on the p-type semiconductor layer 24. Recombination radiation occurs at the maximum immediately under an electrode in which

a current flows at the maximum. However, a general electrode blocks light, and hence it follows that light emitted immediately under the electrode is hardly extracted outward. In this case, it is important to spread the current to a region other than the electrode. Therefore, a current diffusion layer is provided or a thin gold electrode transmitting light is provided on the overall surface.

Fig. 11 is a sectional view of an LED having a current diffusion electrode 26 provided on a p-type semiconductor layer 24. The current diffusion electrode 26 is formed by an Au thin film of about 20 nm in thickness, in order to obtain sufficient current spreading.

However, the transmittance of the Au thin film 26 having this thickness is only 37 % for light of 500 nm in wavelength. Thus, most part of the light is absorbed, to result in inferior luminous efficiency.

Problems of a transparent conductor film applied to a conventional compound semiconductor light-emitting device are now described. The transparent conductor film applied to the conventional compound semiconductor light-emitting device is generally prepared from ITO (In_2O_3 -5 wt.% SnO_2). Sputtering is mainly employed for forming a film of ITO having transmittance of at least 80 % and resistivity of about $2 \times 10^{-4} \Omega\text{cm}$ at a substrate temperature of 300°C with excellent reproducibility. In consideration of application to organic electroluminescence (EL) or a light-emitting diode (LED), a transparent conductor film formable at a lower temperature is demanded.

Japanese Patent Laying-Open No. 6-318406 (1994) proposes a technique of manufacturing a film of In_2O_3 -10 wt.% ZnO capable of implementing high transmittance and low resistivity at the room temperature. According to this technique, a film of 140 nm in thickness formed by sputtering under the room temperature implements resistivity of $3 \times 10^{-4} \Omega\text{cm}$ and transmittance of 86 % (at 550 nm).

Further, study is made on formation of a transparent conductor film by vapor deposition or ion plating.

The transmittance and conductivity of a transparent conductor film remarkably depend on the amount of oxygen. In the conventional vapor

deposition, however, the film forming pressure is disadvantageously limited due to running of a vapor deposition source. In the sputtering, the pressure and the gas for forming a film are limited such that the pressure range is limited for utilizing plasma and argon is required for forming the plasma, and hence the amount of oxygen cannot be precisely controlled.

SUMMARY OF THE INVENTION

The present invention has been proposed in order to solve the aforementioned problems, and an object thereof is to provide a semiconductor light-emitting device improved to be capable of increasing luminous efficiency.

Another object of the present invention is to provide a method of manufacturing a transparent conductor film improved to be capable of precisely controlling the amount of oxygen with no limitation of the film forming pressure and the film forming gas.

Still another object of the present invention is to provide a method of manufacturing a transparent conductor film improved to be capable of improving transmittance and reducing the cost.

A further object of the present invention is to provide a method of manufacturing a compound semiconductor light-emitting device including a step of forming a transparent conductor film improved to be capable of improving transmittance and reducing the cost.

A semiconductor light-emitting device according to a first aspect of the present invention comprises a substrate provided with an n-type lower electrode on the back surface. A light-emitting layer is provided on the aforementioned substrate. A p-type semiconductor layer is provided on the aforementioned light-emitting layer. An upper electrode is provided on the aforementioned p-type semiconductor layer. The aforementioned upper electrode has a multilayer structure consisting of at least two heterogeneous layers.

According to this aspect, the upper electrode is formed by the multilayer structure consisting of at least two heterogeneous layers, whereby an upper electrode forming no junction with the p-type semiconductor layer and having high transmittance can be formed by

properly selecting the heterogeneous layers. Thus, a semiconductor light-emitting diode having high luminous efficiency can be obtained.

According to a second aspect of the present invention, the
aforementioned upper electrode includes an Au thin film coming into
5 contact with the aforementioned p-type semiconductor layer and an n-type
transparent conductor film formed thereon.

The transparent conductor film, generally prepared from an n-type
semiconductor, inevitably forms a junction when the same is directly
10 formed on the p-type semiconductor layer. In order to avoid this, an
extremely thin Au film is formed on the p-type semiconductor layer, and
the transparent conductor film is thereafter stacked thereon. When the
thickness of the Au thin film is reduced, the transmittance is not
remarkably reduced. The transparent conductor film having high
transmittance can be formed in a relatively large thickness. Consequently,
15 a current effectively spreads over the electrode through the transparent
conductor film.

According to a third aspect of the present invention, the thickness of
the aforementioned Au thin film is 1nm to 3 nm. The thickness of the Au
film is so sufficiently small that the transmittance is not remarkably
20 reduced.

According to a fourth aspect of the present invention, the
aforementioned transparent conductor film is made of In_2O_3 -10 wt.% ZnO .
The transmittance is increased when preparing the transparent conductor
film from such a material.

According to a fifth aspect of the present invention, the
aforementioned upper electrode has a multilayer structure including an
upper layer and a lower layer. The surface of the aforementioned lower
layer is flattened. The surface of the aforementioned upper layer is
25 irregularized.

According to this aspect, light, which is unextractable due to total
reflection when the surface is smooth, can be effectively extracted outward
by irregularly controlling the surface shape of the transparent conductor
30 film. Consequently, optical output can be improved.

According to a sixth aspect of the present invention, the
aforementioned substrate includes a ZnSe single-crystalline substrate.
The aforementioned p-type semiconductor layer includes a ZnSe
semiconductor layer, a ZnTe semiconductor layer or a BeTe semiconductor
5 layer.

According to a seventh aspect of the present invention, the
aforementioned transparent conductor film of In_2O_3 -10 wt.% ZnO is formed
by laser ablation.

10 In a method of manufacturing a transparent conductor film
according to an eighth aspect of the present invention, a substrate is first
placed in a vacuum chamber. A target serving as the material for a
transparent conductor film is centered in the aforementioned vacuum
chamber. Oxygen is introduced into the aforementioned vacuum chamber.
The aforementioned target is irradiated with a laser beam, for depositing
15 atoms or molecular ions emitted by ablation on the aforementioned
substrate and crystal-growing the atoms or molecular ions while oxidizing
the same.

This method, referred to as laser ablation, is employed for forming a
transparent conductor film in the present invention. In the laser ablation,
20 an excimer laser beam serving as an excitation source is guided from
outside a film forming apparatus, not to limit the film forming pressure and
the film forming gas. Further, the laser ablation involves no influence by
reverse sputtering caused by negative ions, dissimilarly to sputtering.
According to the laser ablation, the amount of oxygen can be readily
25 controlled and a film having a composition close to that of the target can be
obtained due to the aforementioned characteristics. Further, it is possible
to obtain a film having superior surface smoothness as compared with that
formed by sputtering.

30 According to a ninth aspect of the present invention, In_2O_3 -10 wt.%
ZnO is employed for the aforementioned target.

A method of manufacturing a transparent conductor film according
to a tenth aspect of the present invention is carried out at a film forming
temperature in the range of the room temperature (RT) to 300°C.

A method of manufacturing a transparent conductor film according to an eleventh aspect of the present invention is carried out at a film forming pressure of 0.3 to 3×10^{-3} Torr.

5 In a method of manufacturing a compound semiconductor light-emitting device according to a twelfth aspect of the present invention, a compound semiconductor light-emitting device substrate immediately before formation of a transparent electrode is first prepared. The
10 aforementioned compound semiconductor light-emitting device substrate is placed in a vacuum chamber. A target serving as the material for a transparent conductor film is centered in the aforementioned vacuum chamber. Oxygen is introduced into the aforementioned vacuum chamber. The aforementioned target is irradiated with a laser beam, for crystal-
15 growing the aforementioned transparent electrode while depositing atoms or molecular ions emitted by ablation on the aforementioned substrate and oxidizing the same.

According to this aspect, the transparent electrode is formed on the compound semiconductor light-emitting device substrate by laser ablation, whereby a compound semiconductor light-emitting device having low resistance and improved transmittance is obtained.

20 According to a thirteenth aspect of the present invention, In_2O_3 -10 wt.% ZnO is employed as the aforementioned target.

A method of manufacturing a compound semiconductor light-emitting device according to a fourteenth aspect of the present invention is carried out at a film forming temperature in the range of the room
25 temperature (RT) to 300°C .

A method of manufacturing a compound semiconductor light-emitting device according to a fifteenth aspect of the present invention is carried out at a film forming pressure of 0.3 to 3×10^{-3} Torr.

30 The foregoing and other objects, features, aspects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 includes diagrams (a) to (d) for illustrating the structure of a semiconductor light-emitting device according to a first embodiment of the present invention;

Fig. 2 is a sectional view of a specific example of the semiconductor light-emitting device according to the first embodiment;

Fig. 3 is a conceptual diagram for illustrating improvement of optical output in a semiconductor light-emitting diode according to a second embodiment of the present invention;

Fig. 4 is a conceptual diagram of a laser ablation film forming apparatus;

Fig. 5 illustrates the relation between resistivity of an IDIXO film having a thickness of 120 nm and oxygen pressure;

Fig. 6 illustrates oxygen pressure dependency of transmittance of the IDIXO film having a thickness of 120 nm;

Fig. 7 illustrates wavelength dependency of transmittance of IDIXO (120 nm)/Au;

Fig. 8 is a sectional view of a compound semiconductor light-emitting device obtained according to the present invention;

Figs. 9A and 9B are sectional views of a conventional light-emitting device;

Fig. 10 is a sectional view showing the structure of a conventional LED; and

Fig. 11 is a sectional view of a conventional LED having a current diffusion electrode.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

First Embodiment

In a first embodiment of the present invention, a transparent conductor film having low resistance and high transmittance is employed in place of a conventional Au film. Referring to (a) of Fig. 1, the transparent conductor film is applied as a p-type electrode in the first embodiment of the present invention in particular.

Referring to (c) of Fig. 1, a transparent conductor film 30 generally consists of an n-type semiconductor, and inevitably forms a junction when

directly formed on a p-type semiconductor layer 24. Referring to (b) of Fig. 1, transmittance as well as luminous efficiency are reduced when an Au film 26 is formed on a p-type semiconductor layer.

Referring to (d) of Fig. 1, an extremely thin Au thin film 10a is
5 formed on a p-type semiconductor layer 24, and a transparent conductor film 10b is thereafter stacked according to the present invention. The Au thin film 10a has a sufficiently small thickness of 1 to 3 nm, so that the transmittance is not remarkably reduced. The transparent conductor film 10b having high transmittance can be formed in a relatively large thickness.
10 Consequently, a current effectively spreads over an electrode through the transparent conductor film 10b.

Fig. 2 is a sectional view of a ZnSe compound semiconductor light-emitting device to which the present invention is applied. An n-type ZnSe buffer layer 2 of 1 μm in thickness, an n-type ZnMgSSe clad layer 3 of 1 μm
15 in thickness, a ZnSe/ZnCdSe multiple quantum well active layer 4, a p-type ZnMgSSe clad layer 5 of 1 μm in thickness, a p-type ZnSe layer 6 of 0.2 μm in thickness and a p-type contact layer 7 consisting of a multilayer superlattice structure of ZnTe and ZnSe are successively provided on a conductive ZnSe single-crystalline substrate 1 having an n-type electrode
20 12 on the back surface. A p-type ZnTe layer 8 of 60 nm in thickness is provided on the uppermost surface. An upper electrode 10 having a multilayer structure of an Au thin film 10a of 1 to 3 nm in thickness and a transparent conductor film 10b formed thereon is formed on such an epi-structure.

25 Second Embodiment

A second embodiment of the present invention relates to a technique of extracting light, which is unextractable due to total reflection, by (e.g., irregularly) controlling the surface shape of a transparent conductor film. Thus, optical output can be improved. Fig. 3 illustrates the concept of this
30 embodiment.

The following equation holds according to the Snell's law:

$$n_1 \sin \theta_1 = n_2 \sin \theta_2$$

Assuming that $n_1 = 3.5$ (semiconductor) and $n_2 = 1$ (air), an angle θ_1

equalizing an angle θ_2 with 90° is 16.6° (critical angle). In this case, therefore, light is only partially extractable outward.

5 The critical angle can be increased by preparing the surface of the transparent conductor film in the form of a lens or a saw. Thus, light unextractable due to total reflection can be extracted outward and optical output can be improved.

10 According to the present invention, as hereinabove described, the transmittance of an electrode is increased and optical output is improved when a p-type electrode is formed by a transparent conductor film and an Au film. Further, the life is increased under constant output due to the improvement of the optical output. The surface shape of the transparent conductor film can be controlled due to the high transmittance thereof. Consequently, the optical output can be improved. In addition, the thickness of the transparent conductor film can be increased due to the high transmittance, and the surface shape thereof can be readily controlled.

The present invention is now more specifically described.

The following Examples were directed to LEDs of ZnSe, and In_2O_3 -10 wt.% ZnO was employed as the material for transparent conductor films.

(Example 1)

20 In each sample, an LED having an active layer of ZnCdSe was formed on an n-type ZnSe(100) substrate prepared by CVT (chemical vapor transport). A p-type electrode was set in a p-type ZnSe/ZnTe superlattice structure. An Au film of 3 nm in thickness was formed thereon by vacuum deposition, and a film of In_2O_3 /10 wt.% ZnO (IDIXO) was thereafter formed by laser ablation under the following conditions:

Film Forming Temperature: room temperature (25°C)

Film Forming Pressure: 3×10^{-3} Torr O_2

Laser: KrF laser of 248 nm and 2 J/cm^2

25 Table 1 shows the relation between electrode structures, voltages and optical output values (in energization at 20 mA).

Table 1

Electrode Structure	Voltage	Optical Output
20nm Au	2.88V	1.31mW
IDIXO(90nm) / Au(3nm)	2.84V	1.91mW
IDIXO(180nm) / Au(3nm)	2.79V	2.19mW
IDIXO(190nm)	3.39V	2.21mW

5 In the sample having the electrode structure of IDIXO (180 nm)/Au (3 nm), the optical output was increased from 1.31 mW to 2.19 mW, i.e., by 1.67 times, as compared with the conventional sample having the electrode structure of Au (20 nm). The operating voltage remained substantially unchanged. In the sample having the electrode structure of IDIXO (190 nm), the optical output was substantially identical to that of the sample
10 having the electrode structure of IDIXO (180 nm)/Au (3 nm) while the operating voltage was increased. Thus, it is understood that the film of Au (3 nm) suppressed formation of a junction.

In the sample having the electrode structure of IDIXO (90 nm)/Au (3 nm), the optical output was reduced as compared with the sample having
15 the electrode structure of IDIXO (180 nm)/Au (3 nm). This conceivably resulted from reduction of transmittance caused by multiple reflection in the film of IDIXO. As a result of an experiment, it has been proved that an electrode structure of IDIXO (180 nm to 200 nm)/Au (2 nm to 3 nm) brings an excellent result.

20 The thickness maximizing the transmittance is expressed as $(1/4 + m/2) \times \lambda/n$ ($m = 0, 1, 2, 3$), and the thickness minimizing the transmittance is expressed as $(m/2) \times (\lambda/n)$, where λ represents the emission wavelength and n represents the refractive index of the IDIXO film.

25 When the emission wavelength of an LED is 480 nm, for example, the actual value of the refractive index n is 2.07. Therefore, the thickness maximizing the transmittance is 58 nm ($m = 0$) or 174 nm ($m = 1$), i.e., substantially identical to the aforementioned thickness of 180 nm. The thickness minimizing the transmittance is 116 nm ($m = 1$).

(Example 2)

After depositing an Au film, a film of $\text{In}_2\text{O}_3/10 \text{ wt.}\%$ ZnO was continuously formed in two stages as shown in Table 2. The surface of the upper layer was irregularized.

5

Table 2

	Film Forming Temperature	Thickness	Film Forming Pressure	Surface Shape
Lower Layer	Room Temperature	180nm	3×10^{-3} Torr	smooth
Upper Layer	Room Temperature	180nm	3×10^{-1} Torr	irregular

In this sample, the operating voltage was 2.80 V, and the optical output was increased to 2.43 mW.

10

Third Embodiment

15

Fig. 4 is a conceptual diagram of a laser ablation apparatus employed for a method of manufacturing a transparent conductor film according to the present invention. A method irradiating a solid surface with a high-density laser pulse, depositing ions or atoms emitted therefrom on a substrate located on an opposite position and forming a thin film is referred to as laser ablation. This method is remarkably suitable for a process of forming a metal oxide derivative thin film. According to laser ablation, not only film formation but also refinement, etching, multi-layering etc. can be performed in the same experimental chamber with a strong laser pulse. In relation to formation of a ferroelectric thin film, laser ablation has the following advantages:

20

25

First, a laser beam is introduced from outside a film forming chamber in this method, and hence a thin film can be formed in an arbitrary atmosphere pressure suitable for crystal growth. Further, atoms, molecules and ions are emitted only from a target, and hence a thin film containing no impurities is formed.

Various parameters such as a pressure, a substrate temperature and a film forming rate can be individually selected. As to controllability of the thin film, film formation can be instantaneously controlled by adjusting

the number of laser pulses and energy.

In addition, the film can be formed at an extremely high rate, as recently clarified. Thus, laser ablation has a number of excellent points.

5 In the laser ablation apparatus, a bulk target consisting of a ferroelectric substance for forming a film is centered in a vacuum chamber, oxygen or gas such as ozone or NO₂ having strong oxidizing power is introduced into the vacuum system and the target is irradiated with a pulse laser beam for causing ablation. Atoms, molecules and ions emitted by ablation are deposited on the substrate and oxidized to cause crystal
10 growth. In a third embodiment of the present invention, a transparent conductor film was formed in such a laser ablation apparatus.

Film forming conditions are as follows:

Laser: KrF laser of 248 nm and 2-J/cm²

15 Target: In₂O₃-10 wt.% ZnO (high-density product by Idemitsu Kosan, hereinafter abbreviated as IDIXO)

Substrate: MgO (for measuring transmittance) and glass (for measuring resistivity)

Film Forming Temperature: in the range of the room temperature (RT) to 300°C, the room temperature was employed as the standard

20 Film Forming Pressure: 0.3 to 3 × 10⁻³ TorrO₂, 3 × 10⁻³ TorrO₂ was employed as the standard

The film forming conditions are described in more detail.

Oxygen Pressure Dependency

25 Oxygen pressure dependency of the transmittance of the transparent conductor film was first investigated.

In laser ablation, a plume shape varies with the film forming pressure. Under a certain film forming pressure, the composition of a sample varies with the part of the plume on which the sample is located. The inventor has obtained such a result that the optimum target-to-
30 substrate distance is about 60 to 70 mm in development of a superconducting device, and this time the target-to-substrate distance was fixed to 70 mm from observation of the plume, for evaluating the oxygen pressure dependency. The thickness of the IDIXO film was set to about

120 nm.

Fig. 5 shows the relation between resistivity and oxygen pressure. The resistivity remarkably varied with the oxygen pressure, and a characteristic having a dip was obtained at 3×10^{-3} Torr. This agrees with the hitherto report that the resistance of a transparent conductor film has an optimum value depending on the amount of oxygen. It is extremely difficult to obtain the lowest value of $6.5 \times 10^{-5} \Omega\text{cm}$ by conventional sputtering. Thus, the resistivity is lower by about one figure as compared with sputtering, and hence the necessary thickness can be reduced to 1/10, the transmittance can be increased and a high-quality transparent conductor film can be prepared at a low cost. The surface of the film prepared under these conditions was extremely smooth with roughness of about 0.5 nm. This value is about 1/10 that of a film prepared by sputtering.

Fig. 6 shows results of evaluation of the oxygen pressure dependency of transmittance of IDIXO. An absorption edge of about 300 nm is recognized. The sample for measuring transmittance was formed by a substrate of MgO having an absorption edge of about 200 nm, and it is understood that absorption of 300 nm results from the IDIXO film. The transmittance of the MgO substrate measured at a wavelength of 500 nm was 84 %, and the relation between the film forming oxygen pressure and the transmittance of the IDIXO film (120 nm) can be calculated as shown in Table 3.

Table 3

Film Forming Oxygen Pressure	IDIXO Transmittance
0.3 Torr	99%
3×10^{-3} Torr	87%
3×10^{-3} Torr	92%

At the film forming pressure of 0.3 Torr, the resistivity is extremely increased although high transmittance is obtained. At the film forming

pressure of 3×10^{-3} Torr providing the lowest resistivity, transmittance of 92 % was obtained. In a light-emitting device, an Au film having a small thickness is generally used as a transparent electrode. The transmittance of an Au film having a thickness of 20 nm is 37 %, and hence optical output exceeding twice that of a general light-emitting device can be obtained by employing an IDIXO film.

Reproducibility

When applying IDIXO to a product, 1) in-plane distribution, 2) reproducibility and the like must be evaluated. A ZnSe substrate, which is about 10 mm square at present, is regarded as non-problematic in consideration of experiments and achievements made by the inventor in relation to plume shapes and superconduction. This time change of resistivity was evaluated on samples of an IDIXO film formed three times under the same conditions. Table 4 shows the results.

Table 4

Run No.	IDIXO Resistivity
1	$3.5 \times 10^{-5} \Omega \text{cm}$
2	$4.5 \times 10^{-5} \Omega \text{cm}$
3	$5.7 \times 10^{-5} \Omega \text{cm}$

While increase of the resistivity is recognized along the run, values of not more than $10^{-5} \Omega \text{cm}$ were obtained in all samples. Stability at such low values is regarded as non-problematic in application to a product.

Conceivable factors for such change of the resistivity are 1) change of the target composition and 2) change of the film thickness. The change 1) can be prevented by polishing the surface of the target and the change 2) can be prevented by setting a thickness monitor.

Oxygen Pressure Dependency in Temperature Reduction

When forming an IDIXO film by laser ablation, the resistivity can be reduced by about one figure in best data as compared with the conventional sputtering. This shows such a possibility that the thickness of a transparent conductor film employed in general can be reduced to about

1/10. In vapor deposition or sputtering, an excitation source (heat in vapor deposition or plasma in sputtering) is present in an apparatus to limit film forming conditions. In contrast with this, an excimer laser beam serving as an excitation source is introduced from outside the apparatus in laser ablation, and hence the film forming pressure can be readily changed from a level close to the atmospheric pressure up to a high vacuum, to enable film formation under the optimum oxygen pressure. Also as to the film composition, a film having a composition close to the target composition can be readily obtained in laser ablation. For such reasons, it is conceivable that formation of an IDIXO film having extremely high resistivity can be implemented according to the present invention.

In order to study the relation between the amount of oxygen and the resistivity, an experiment of changing a temperature reduction atmosphere after film formation was performed at about 300°C. The film forming pressure was set at the value of 3×10^{-3} Torr bringing the lowest resistivity in film formation at the room temperature. Table 5 shows the results.

Table 5

Temperature Reduction Condition	Resistivity
100 Torr O ₂	$5.2 \times 10^{-2} \Omega \text{cm}$
3×10^{-3} Torr (film forming pressure)	$2.3 \times 10^{-3} \Omega \text{cm}$
in vacuo	$1.4 \times 10^{-3} \Omega \text{cm}$

When the temperature was lowered under 100 Torr, oxygen was supplied to the sample to increase the resistivity. When the temperature was reduced under the film forming pressure or in vacuo, the resistivity remained substantially unchanged, to prove that the introduced oxygen is not extremely changed in this temperature range. Also in consideration of the experiment on oxygen pressure dependency, it is conceivable that the IDIXO film facilitates introduction of oxygen, and is hard to damage.

Evaluation of IDIXO Film provided with Au Film

A transparent conductor film, which consists of an n-type semiconductor in general, has a possibility of forming a junction when

formed as a p electrode of a ZnSe LED. In order to avoid this, an IDIXO/Au structure obtained by forming an Au film before forming an IDIXO film was studied. Table 6 shows results in relation to resistivity.

5 Table 6

Thickness of Au Film	Resistivity
3nm	$1.2 \times 10^{-4} \Omega \text{cm}$
10nm	$6.0 \times 10^{-5} \Omega \text{cm}$

As compared with a smooth glass substrate, slightly higher resistivity was obtained when the thickness of the Au film was 3 nm, and substantially identical resistivity was obtained when the thickness was 10 nm. When the thickness of the Au film was 3 nm, the resistivity was increased conceivably because no continuous film of IDIXO was grown in an initial stage due to the presence of Au (not contributing to electrical conduction) grown in the form of an island. When the thickness of the Au film was 10 nm, Au formed a continuous film to compensate for reduction of electrical conduction of IDIXO.

Fig. 7 shows transmittance in IDIXO/Au electrode structures. Reduction of transmittance resulting from the presence of Au is recognized. At a wavelength of 500 nm, the transmittance of the sample of IDIXO (120 nm)/Au (3 nm) is about 80 %. The transmittance was reduced as compared with the small thickness of the Au film conceivably because the transmittance of the IDIXO film itself was reduced due to the presence of Au.

Fourth Embodiment

Fig. 8 is a sectional view of a compound semiconductor light-emitting device manufactured with application of the method of manufacturing a transparent conductor film according to the third embodiment of the present invention. Referring to Fig. 8, an n electrode 52 is provided on the back surface of an n-type semiconductor layer 51. An active layer 53 is provided on the n-type semiconductor layer 51. A p-type semiconductor layer 54 is provided on the active layer 53. A contact layer 55 is provided

on the p-type semiconductor layer 54. A p electrode 6, which is a transparent electrode, is provided on the contact layer 55. A pad 57 is provided on the p electrode 56. A current is fed to the pad 57 from an external power source (not shown) through a wire 58.

5 When employing the laser ablation described with reference to the third embodiment as the method of forming the transparent electrode 56 of the compound semiconductor light-emitting device shown in Fig. 8, an electrode having high transmittance, low electrical conductivity and optical output of at least twice that of a conventional Au electrode is obtained. A
10 ZnSe LED preferably has an electrode structure of IDIXO (200 nm)/Au (3 nm), in order to maximize the optical output.

 The method of manufacturing a transparent conductor film having the aforementioned structure is not only applied to a method of forming a transparent electrode of a compound semiconductor light-emitting device
15 but also employable in various fields of liquid crystal displays, solar batteries and the like.

 Although the present invention has been described and illustrated in detail, it is clearly understood that the same is by way of illustration and example only and is not to be taken by way of limitation, the spirit and
20 scope of the present invention being limited only by the terms of the appended claims.

WHAT IS CLAIMED IS:

1. A semiconductor light-emitting device comprising:
a substrate provided with an n-type lower electrode on the back
surface;
a light-emitting layer provided on said substrate;
5 a p-type semiconductor layer provided on said light-emitting layer;
and
an upper electrode provided on said p-type semiconductor layer,
wherein
said upper electrode has a multilayer structure consisting of at least
10 two heterogeneous layers.
2. The semiconductor light-emitting device according to claim 1,
wherein said upper electrode includes an Au thin film coming into contact
with said p-type semiconductor layer and an n-type transparent conductor
film formed on said Au thin film.
3. The semiconductor light-emitting device according to claim 2,
wherein the thickness of said Au thin film is 1nm to 3 nm.
4. The semiconductor light-emitting device according to claim 2,
wherein said transparent conductor film is made of In_2O_3 -10 wt.% ZnO.
5. The semiconductor light-emitting device according to claim 1,
wherein said upper electrode has a multilayer structure including an upper
layer and a lower layer,
the surface of said upper layer is flattened, and
5 the surface of said upper layer is irregularized.
6. The semiconductor light-emitting device according to claim 1,
wherein said substrate includes a ZnSe single-crystalline substrate, and
said p-type semiconductor layer includes a ZnSe semiconductor layer,

a ZnTe semiconductor layer or a BeTe semiconductor layer.

7. The semiconductor light-emitting device according to claim 4, wherein said transparent conductor film of In_2O_3 –10 wt.% ZnO is formed by laser ablation.

8. A method of manufacturing a transparent conductor film comprising steps of:

- 5 placing a substrate in a vacuum chamber;
 centering a target serving as the material for a transparent
conductor film in said vacuum chamber;
 introducing oxygen into said vacuum chamber; and
 irradiating said target with a laser beam, depositing atoms or
molecular ions emitted by ablation on said substrate and crystal-growing
said atoms or molecular ions while oxidizing said atoms or molecular ions.

9. The method of manufacturing a transparent conductor film according to claim 8, wherein said target contains In_2O_3 –10 wt.% ZnO.

10. The method of manufacturing a transparent conductor film according to claim 9, performing said crystal growth while controlling the film forming temperature in the range of the room temperature to 300°C .

11. The method of manufacturing a transparent conductor film according to claim 8, performing said crystal growth while setting the film forming pressure at 0.3 to 3×10^{-3} Torr.

12. A method of manufacturing a compound semiconductor light-emitting device comprising steps of:

- preparing a compound semiconductor light-emitting device substrate
immediately before formation of a transparent electrode;
5 placing said compound semiconductor light-emitting device substrate
in a vacuum chamber;

centering a target serving as the material for a transparent conductor film in said vacuum chamber;

introducing oxygen into said vacuum chamber; and

- 10 irradiating said target with a laser beam, depositing atoms or molecular ions emitted by ablation on said compound semiconductor light-emitting device substrate and crystal-growing said transparent electrode while oxidizing said atoms or molecular ions.

13. The method of manufacturing a compound semiconductor light-emitting device according to claim 12, wherein said target contains In_2O_3 —10 wt.% ZnO .

14. The method of manufacturing a compound semiconductor light-emitting device according to claim 12, performing said crystal growth while setting the film forming temperature in the range of the room temperature to 300°C .

15. The method of manufacturing a compound semiconductor light-emitting device according to claim 12, performing said crystal growth while setting the film forming pressure at 0.3 to 3×10^{-3} Torr.

ABSTRACT OF THE DISCLOSURE

5 A light-emitting layer is provided on a substrate. A p-type semiconductor layer is provided on the light-emitting layer. An upper electrode is provided on the p-type semiconductor layer. The upper electrode includes an Au thin film coming into contact with the p-type semiconductor layer and an n-type transparent conductor film formed thereon. The n-type transparent conductor film is formed by laser ablation.

FIG. 1

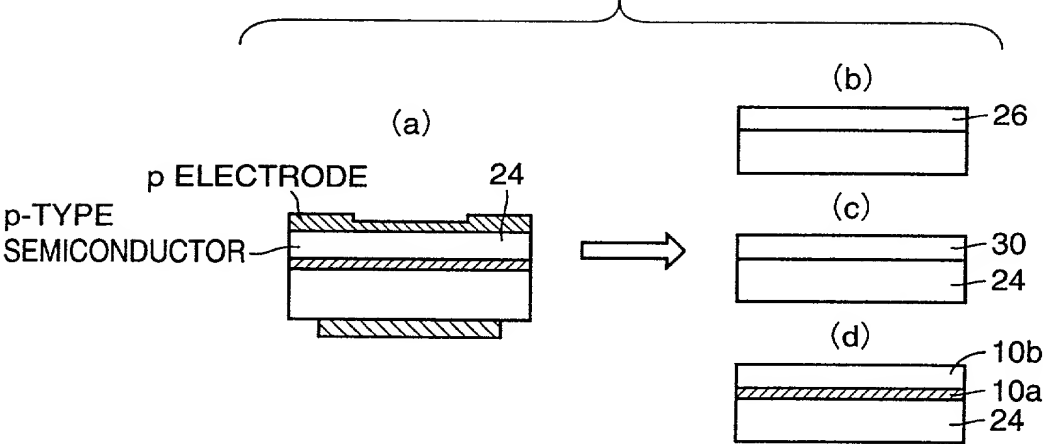


FIG. 3

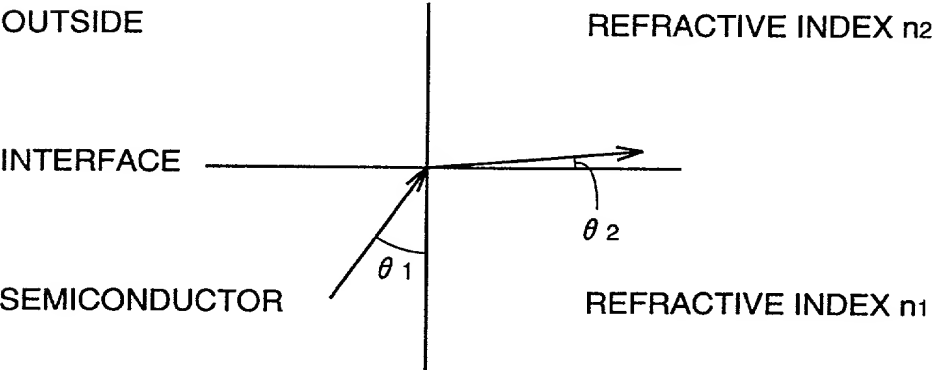


FIG. 2

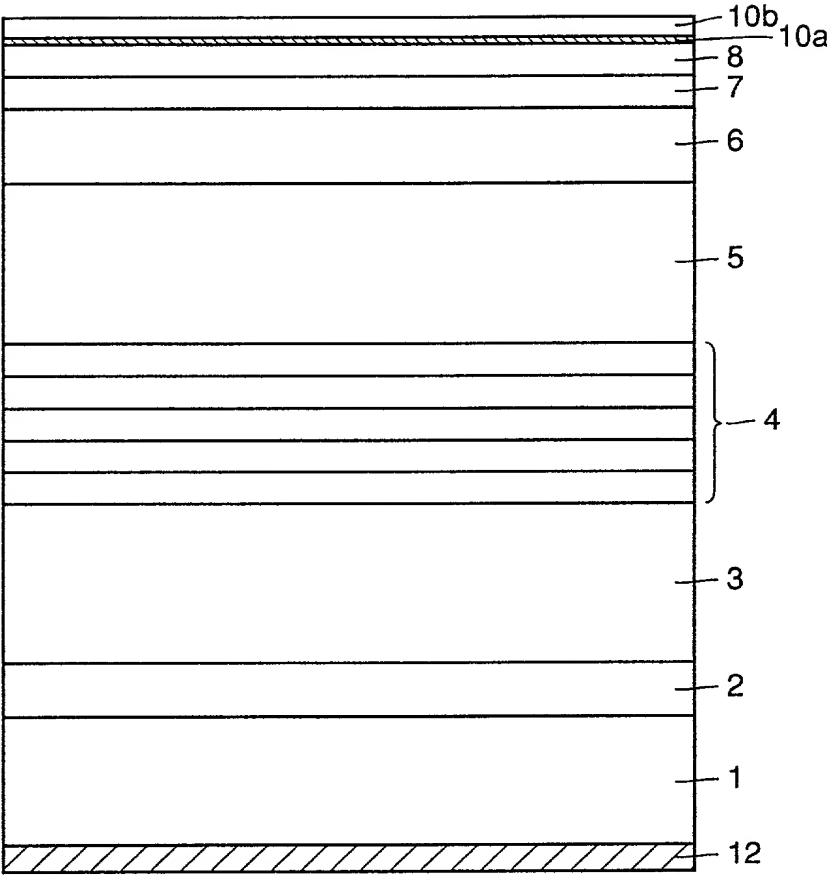


FIG. 4

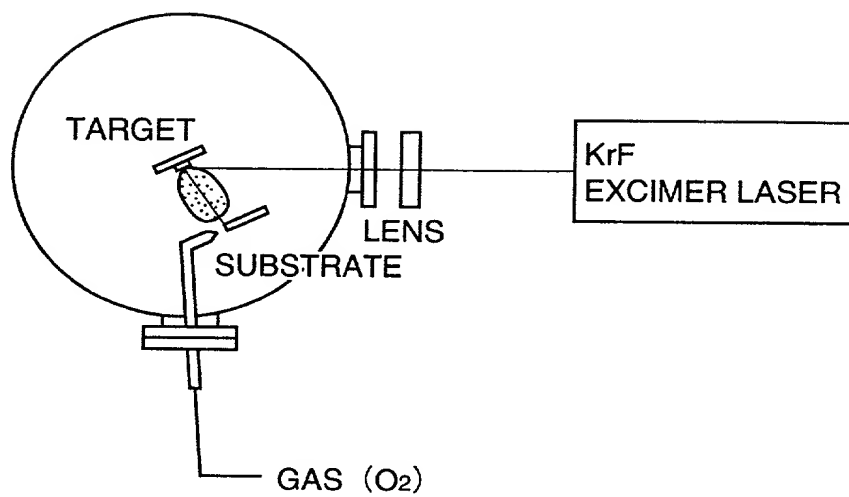


FIG. 8

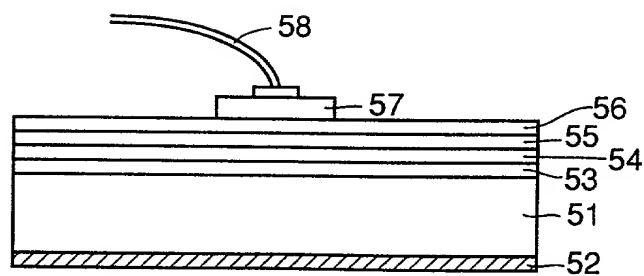


FIG. 5

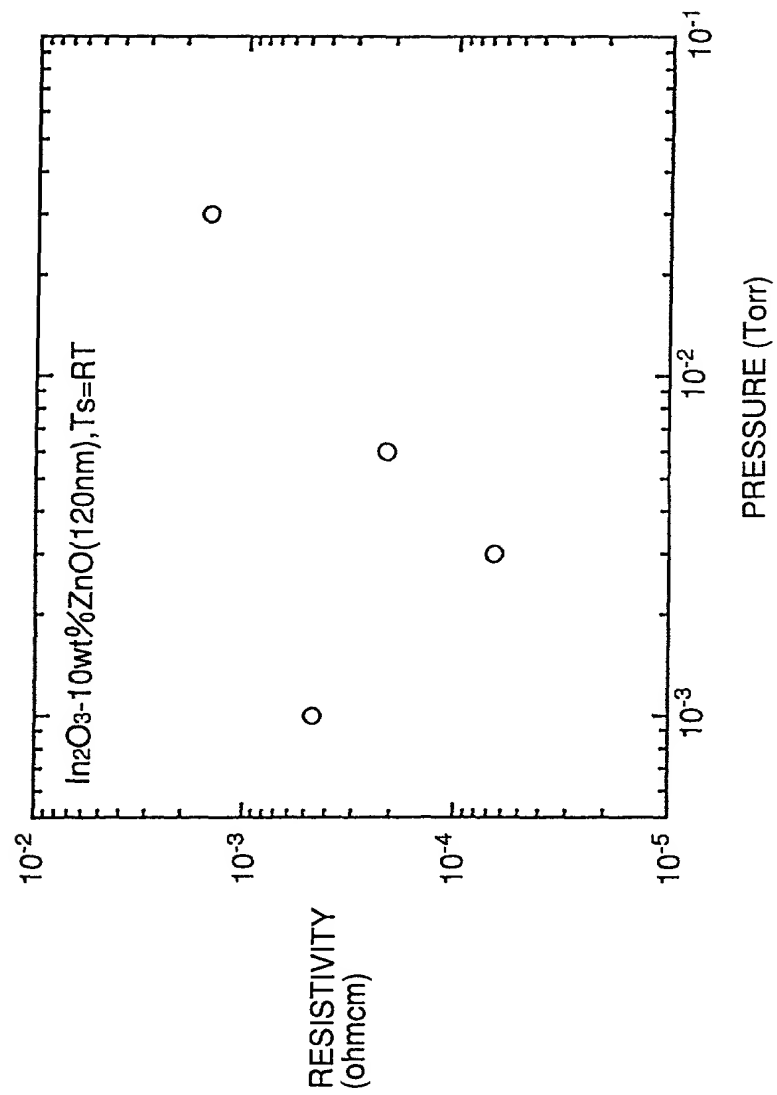


FIG. 6

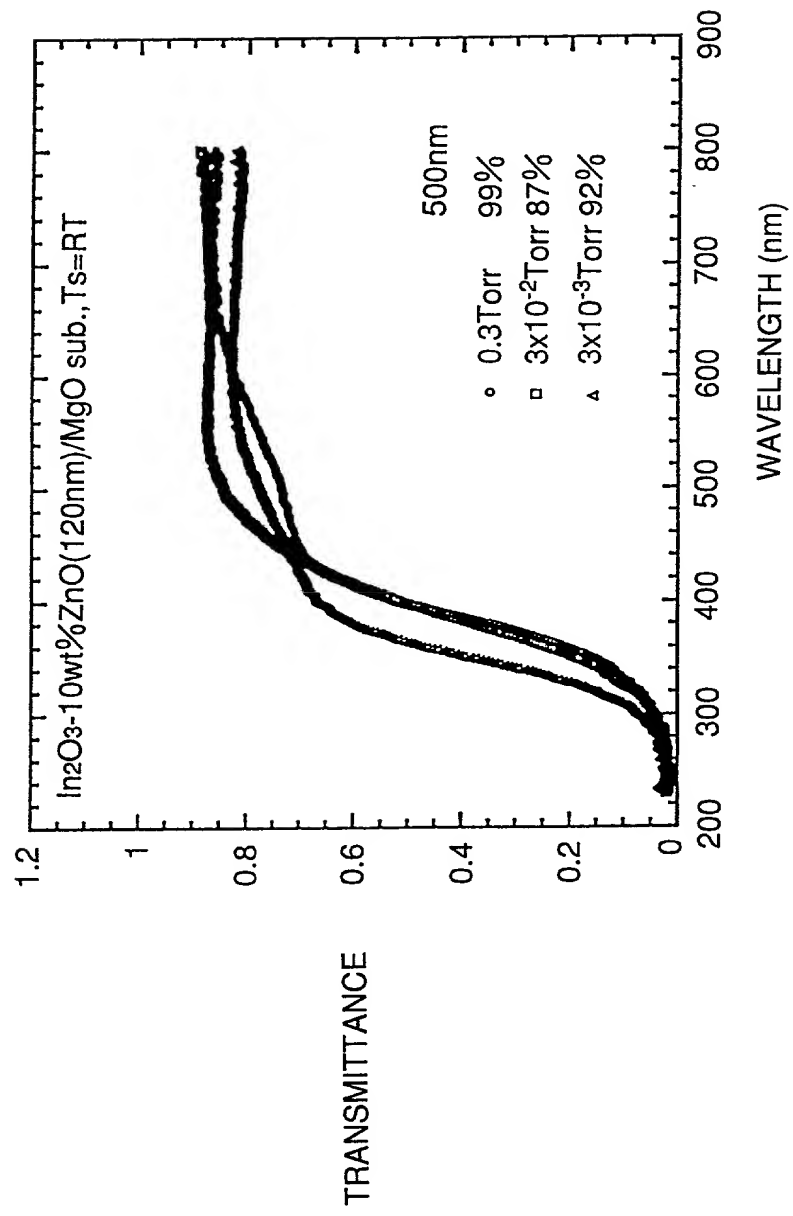


FIG. 7

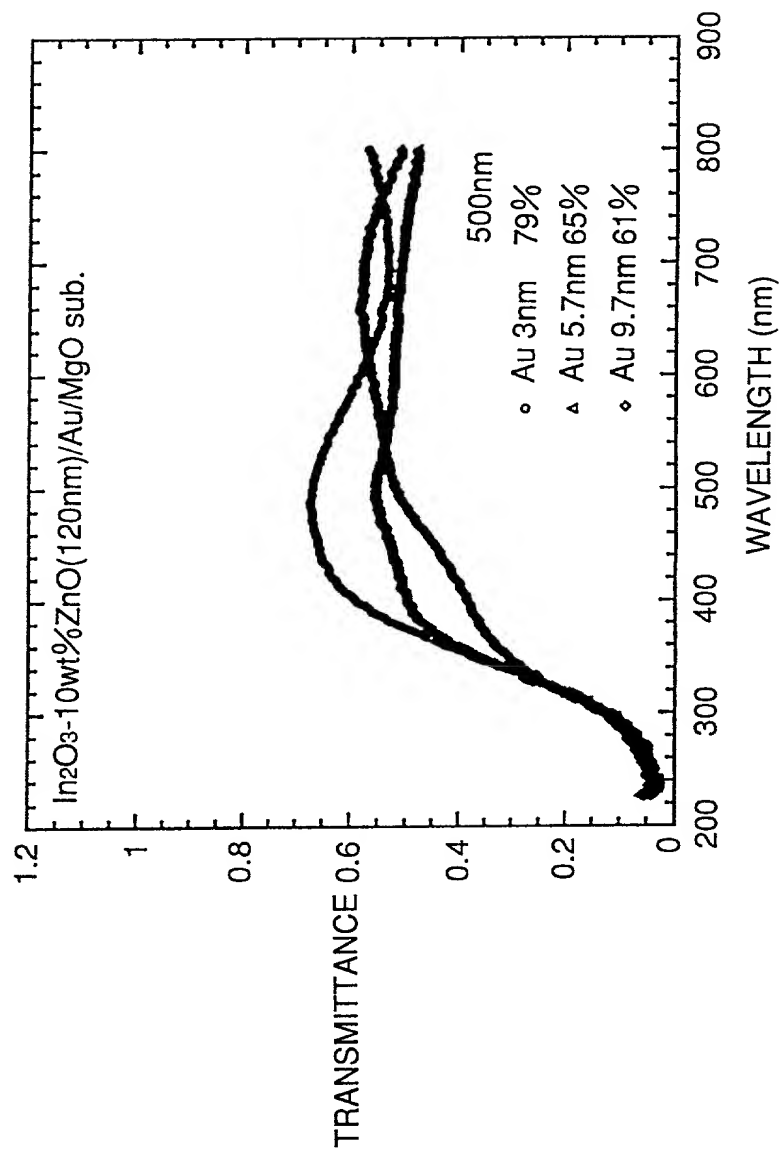


FIG. 9A PRIOR ART

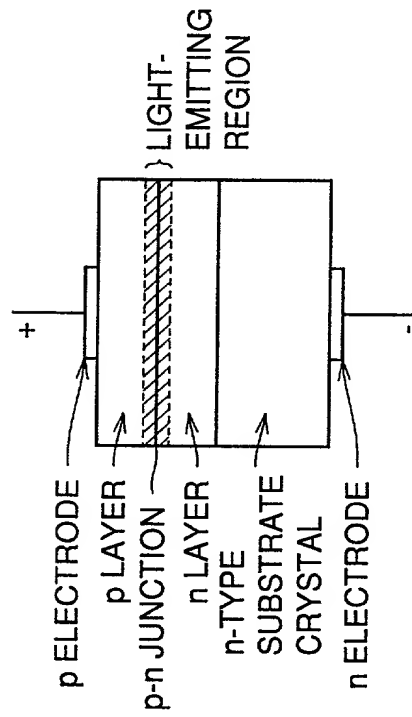


FIG. 9B PRIOR ART

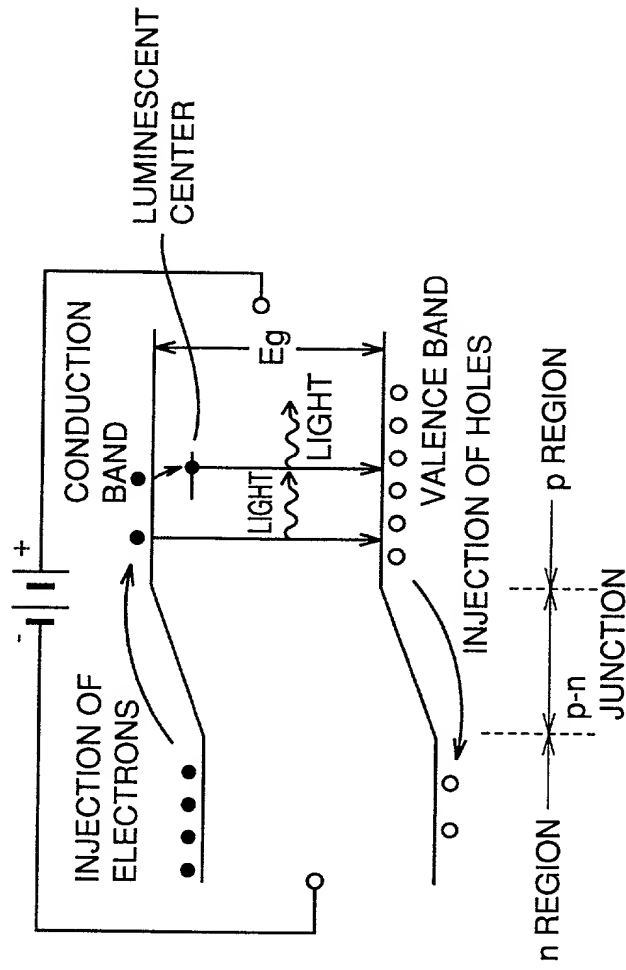


FIG. 10 PRIOR ART

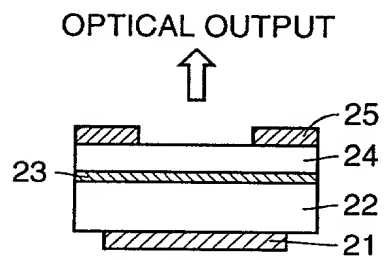
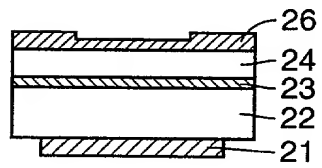


FIG. 11 PRIOR ART



Exp. # F12800002910
Date: March 3, 2000

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PTO/SB/106 (8-96)

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I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

SEMICONDUCTOR LIGHT-EMITTING DEVICE,
METHOD OF MANUFACTURING TRANSPARENT
CONDUCTOR FILM AND METHOD OF
MANUFACTURING COMPOUND SEMICONDUCTOR
LIGHT-EMITTING DEVICE

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☐ was filed on _____
as United States Application Number or
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_____ and was amended to
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Prior Foreign Application(s)

外国での先行出願

11-107586(P)

Japan

(Number)

(Country)

(番号)

(国名)

11-181414(P)

Japan

(Number)

(Country)

(番号)

(国名)

I hereby claim foreign priority under Title 35, United States Code, Section 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Not Claimed

優先権主張なし

15/April/1999

(Day/Month/Year Filed)

(出願年月日)

28/June/1999

(Day/Month/Year Filed)

(出願年月日)

☐

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(Filing Date)

(出願日)

(Application No.)

(出願番号)

(Filing Date)

(出願日)

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(Application No.)

(出願番号)

(Filing Date)

(出願日)

(Status: Patented, Pending, Abandoned)

(現況: 特許許可済、係属中、放棄済)

(Application No.)

(出願番号)

(Filing Date)

(出願日)

(Status: Patented, Pending, Abandoned)

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人の氏名及び登録番号を明記のこと)

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(Supply similar information and signature for third and subsequent
joint inventors.)